

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chilimbi et al.

Application No. To be assigned

Filed: Concurrently herewith

Confirmation No. To be assigned

For: CACHE-CONSCIOUS COALLOCATION
OF HOT DATA STREAMS

Examiner: To be assigned

Art Unit: To be assigned

Attorney Reference No. 3382-66145

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)

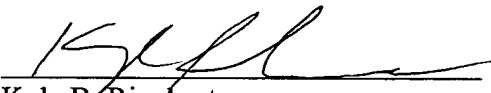
Listed on the accompanying form PTO-1449 and enclosed herewith are several English-language and/or non-English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent.

Applicants filed this Information Disclosure Statement ("IDS") within three months of the filing date of a national application, within three months of the date of entry of the national stage as set forth in § 1.491 in an international application, before the mailing date of a first Office action on the merits, or before the mailing of a first Office action after the filing of request for continued examination under § 1.114. As a result, no fee should be required to file this IDS. However, if the Patent Office determines that a fee is required for Applicants to file this IDS, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550. A **duplicate** copy of this Information Disclosure Statement is enclosed.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

KLARQUIST SPARKMAN, LLP

By 
Kyle B. Rinehart
Registration No. 47,027

One World Trade Center, Suite 1600
121 S.W. Salmon Street
Portland, Oregon 97204
Telephone: (503) 226-7391
Facsimile: (503) 228-9446

INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Attorney Docket Number	3382-66145
		Application Number	To be assigned
		Filing Date	Concurrently herewith
		First Named Inventor	Chilimbi et al.
		Art Unit	To be assigned
		Examiner Name	To be assigned
Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS	
		Kistler et al., "Automated Data-Member Layout of Heap Objects to Improve Memory-Hierarchy Performance," <i>ACM Transactions on Programming Language and Systems</i> , 16 pp. (2000).	
		Halldorsson, "Approximations of Weighted Independent Set and Hereditary Subset Problems," <i>JGAA</i> , Vol. 4, No. 1, pp. 1-16 (April 2000).	
		Hirzel et al., "Bursty Tracing: A Framework for Low-Overhead Temporal Profiling," <i>Microsoft Research</i> , 10 pp. (2001).	
		Gloy et al., "Procedure Placement Using Temporal-Ordering Information," <i>ACM Transactions on Programming Languages and System</i> , Vol. 21, pp.111-161 (1999).	
		Chilimbi et al., "Cache-Conscious Structure Definition," <i>Proc. ACM SIGPLAN '99 Conf. on Programming Language Design and Impl.</i> , 12 pp. (May 1999).	
		Chilimbi et al., "Cache-Conscious Structure Layout," <i>Proc. ACM SIGPLAN '99 Conf. on Programming Language Design and Impl.</i> , 12 pp. (May 1999).	
		Seidl et al., "Segregating Heap Objects by Reference Behavior and Lifetime," <i>Eighth Int'l Conf. on Architectural Support for Programming Languages and Operating Systems</i> , 12 pp. (October 1998).	
		Petrack et al., "The Hardness of Cache Conscious Data Placement," <i>29th Annual ACM Symposium on Principles of Programming Languages</i> , 33 pp. (2002).	
		Rubin et al., "An Efficient Profile-Analysis Framework for Data-Layout Optimizations," <i>POPL</i> , 14 pp. (January 2002).	
		Chilimbi, "On the Stability of Temporal Data Reference Profiles," <i>Int'l Conf. on Parallel Architectures and Compilation Techniques</i> , 10 pp. (September 2001).	
		Chilimbi, "Efficient Representations and Abstractions for Quantifying and Exploiting Data Reference Locality," <i>ACM SIGPLAN '01 Conf. on Programming Language Design and Implementation</i> , 12 pp. (June 2001).	
		Calder et al., "Cache-Conscious Data Placement," <i>ACM</i> , 11 pp. (1998).	
		Berger et al., "Composing High-Performance Memory Allocators," <i>ACM</i> , 11 pp. (2001).	
		Crescenzi et al., "A Compendium of NP Optimization Problems," 20 pp. [Downloaded from the World Wide Web on December 12, 2003].	
		Truong et al., "Improving Cache Behavior of Dynamically Allocated Data Structures," <i>PACT</i> , 8 pp. (1998).	
EXAMINER SIGNATURE:		DATE CONSIDERED:	
* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.			

EXAMINER SIGNATURE:	DATE CONSIDERED:
<p>* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	